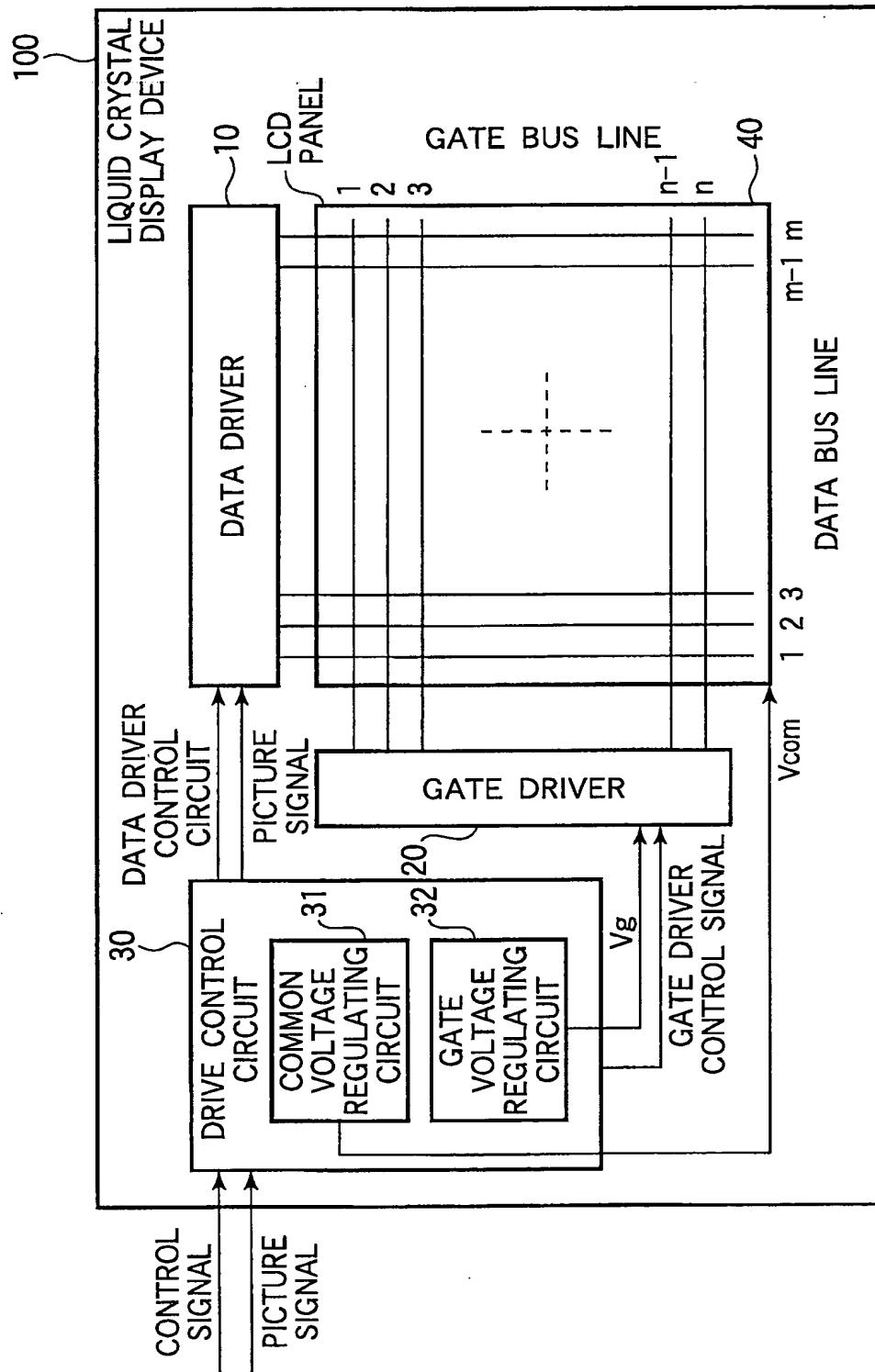


FIG. 1



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FIG.2

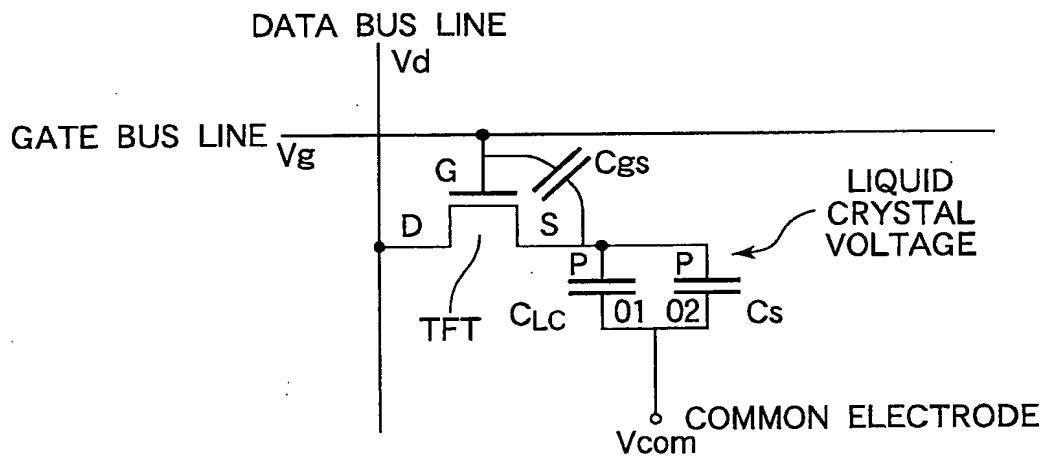
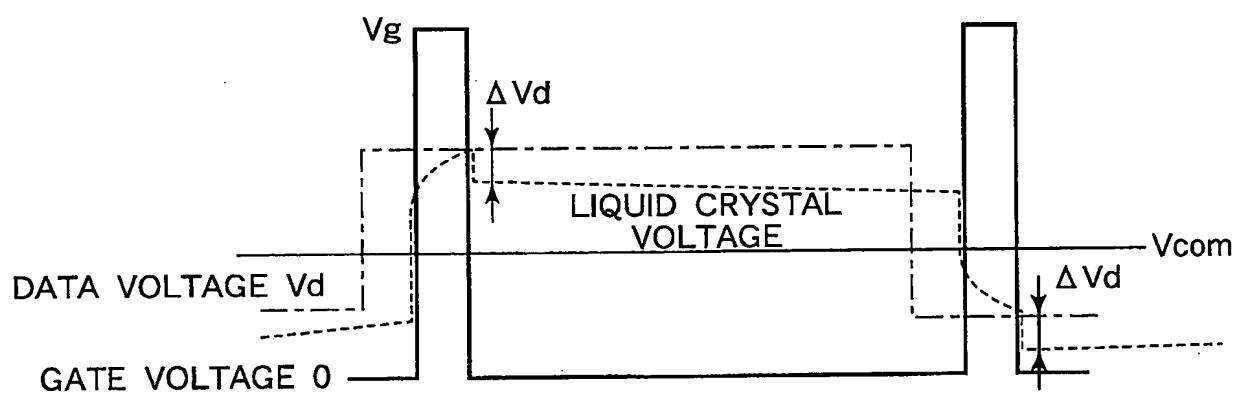


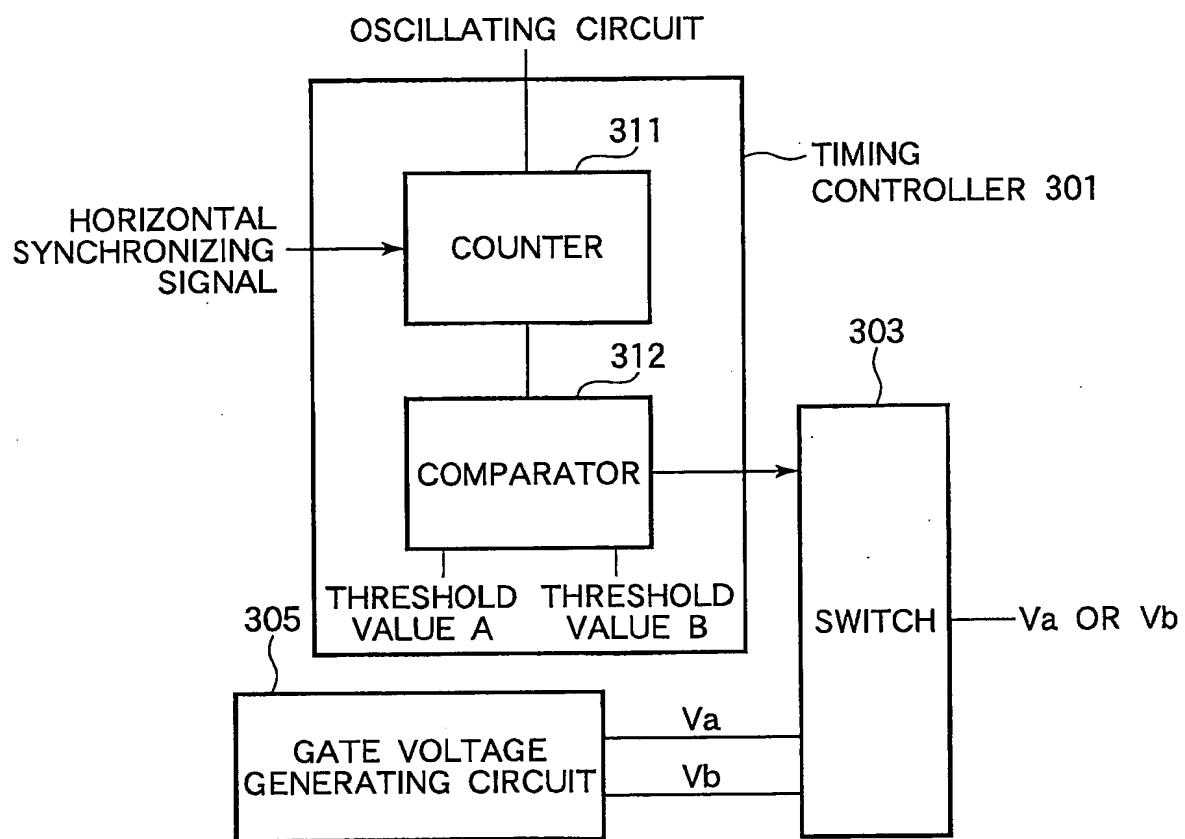
FIG.3



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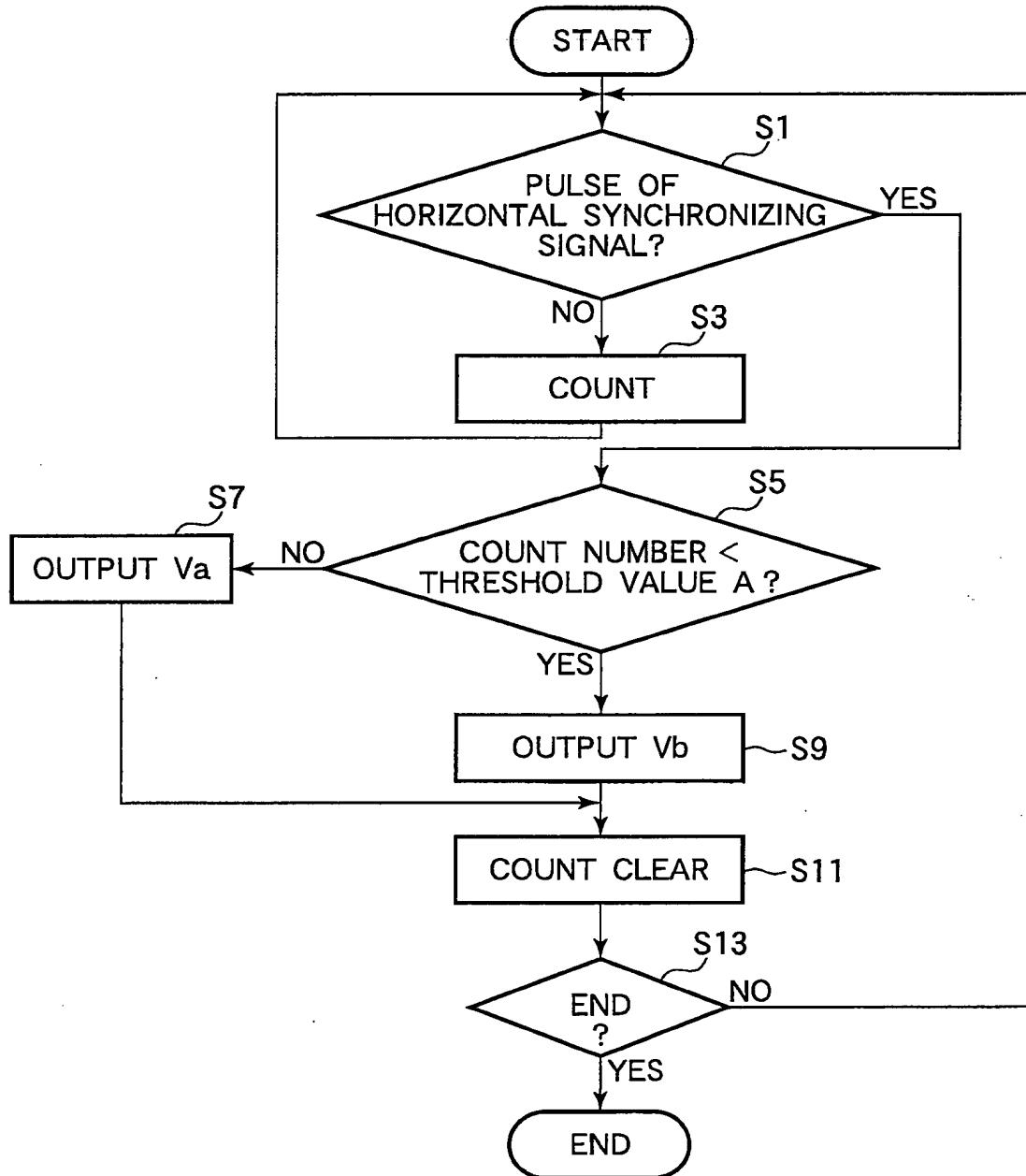
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FIG.4



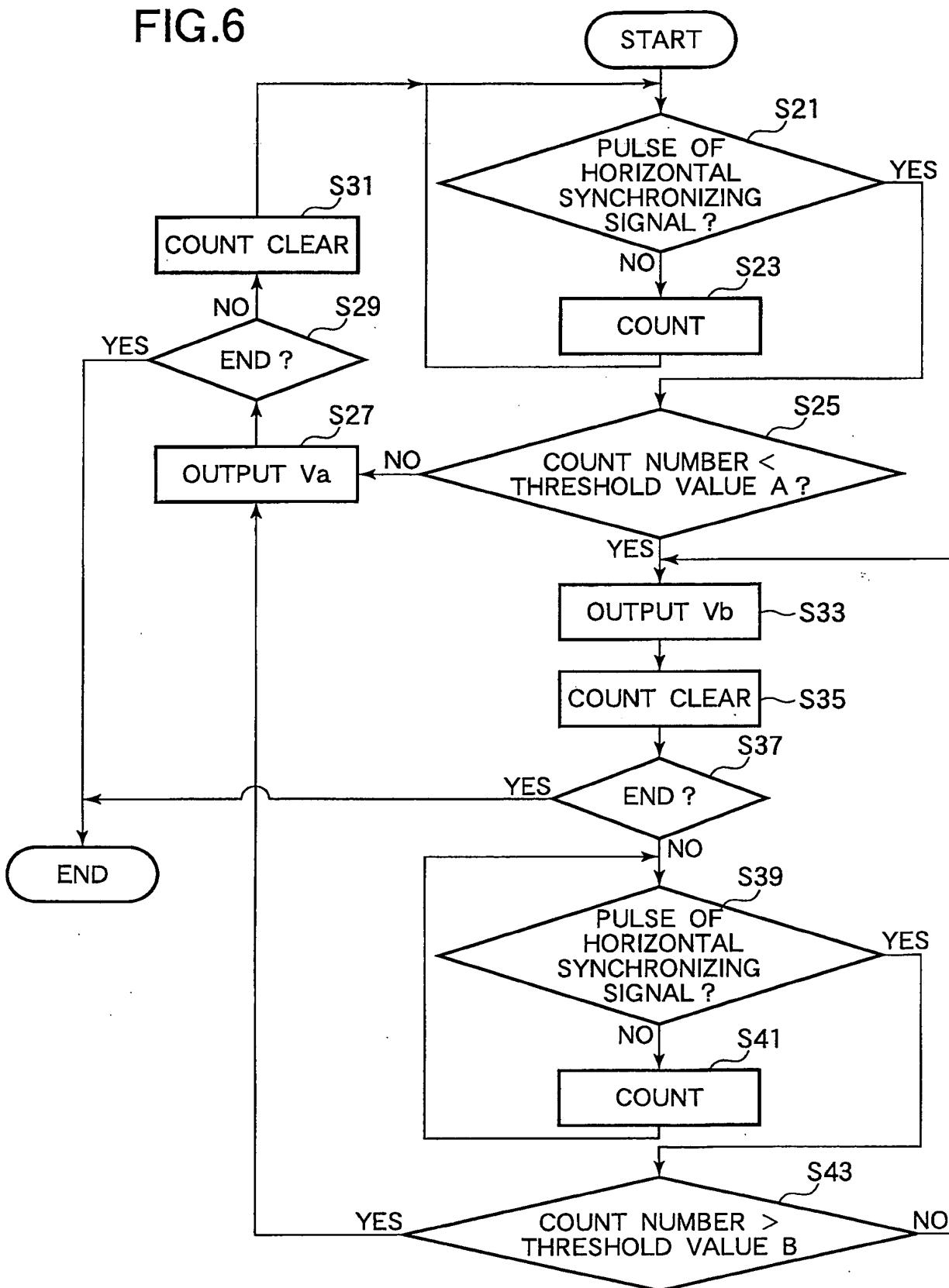
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FIG.5



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FIG.6



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FIG.7A

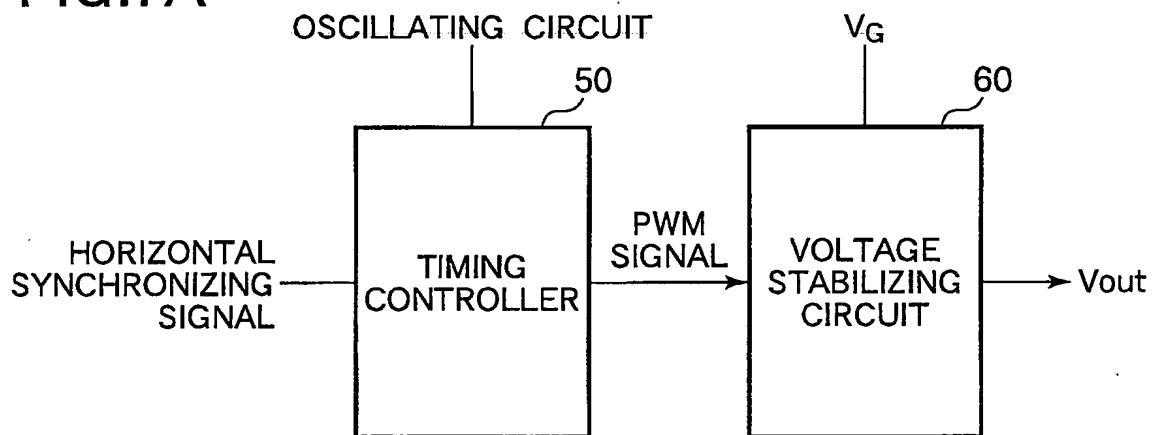


FIG.7B

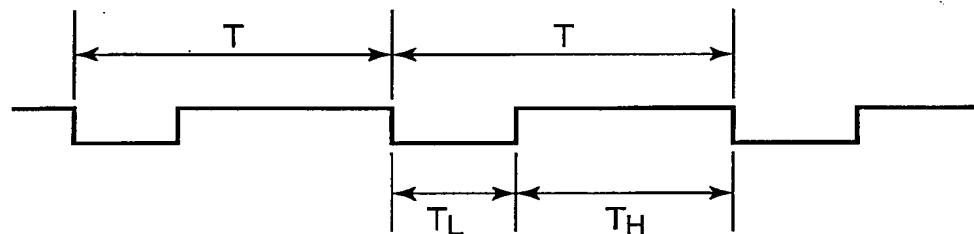
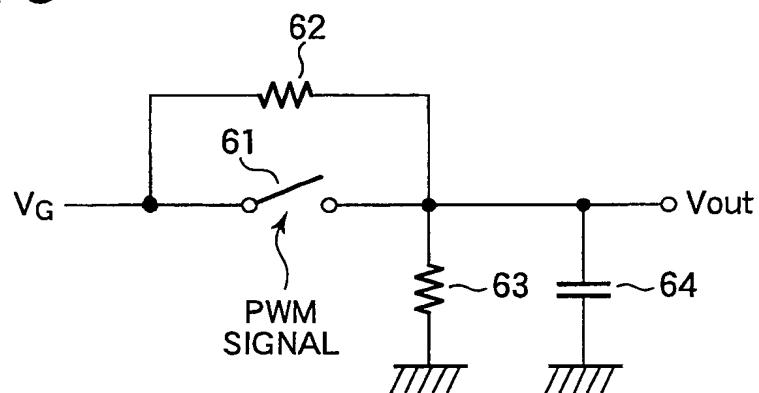


FIG.7C



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FIG.8A

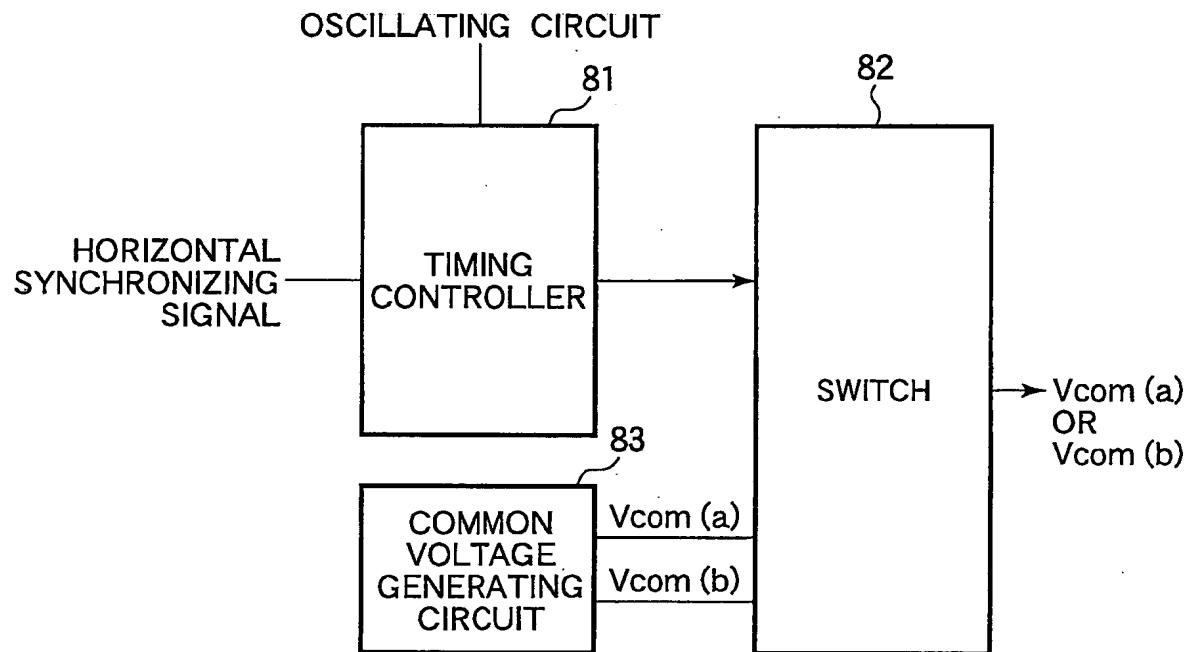
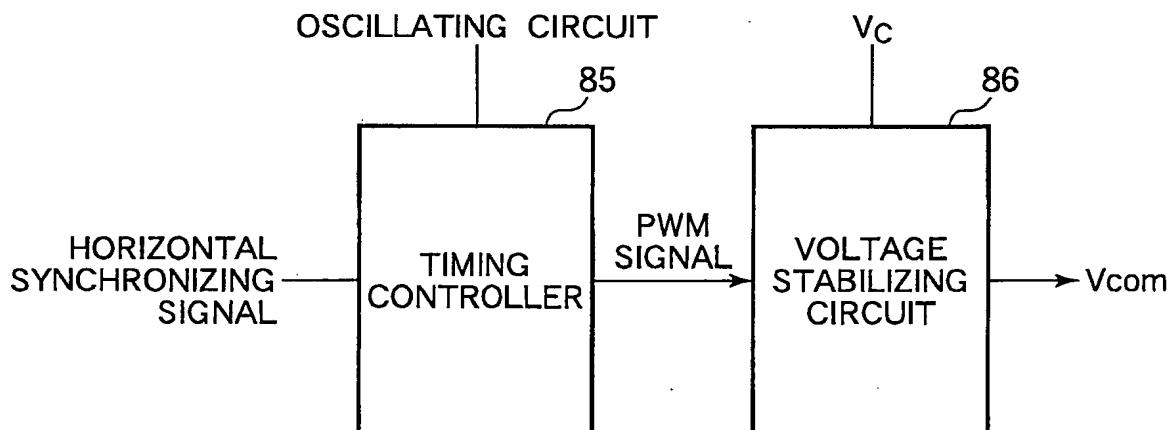
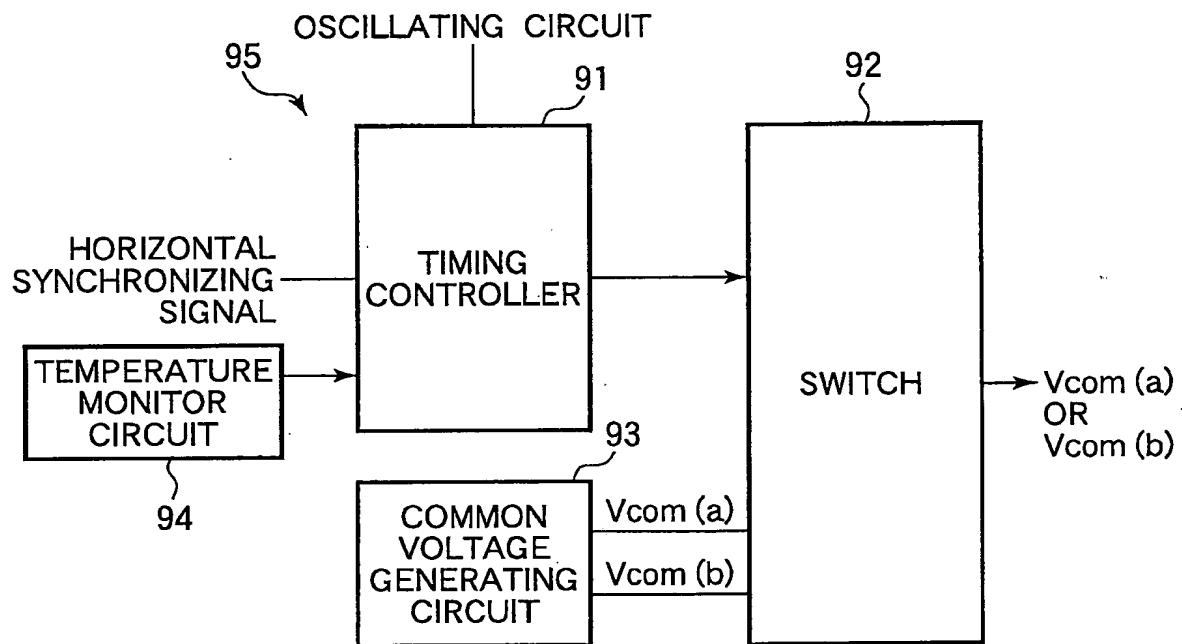


FIG.8B



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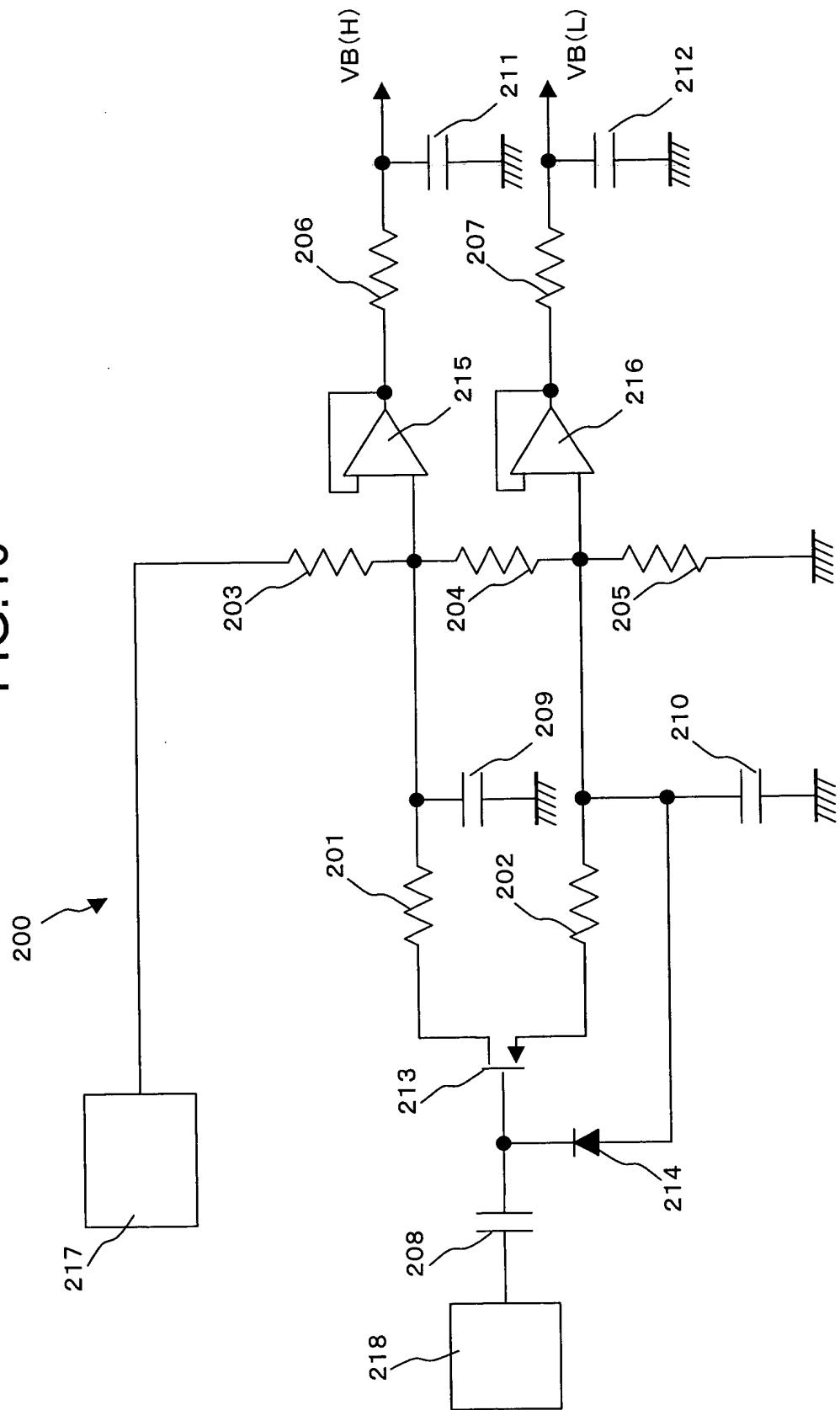
FIG.9



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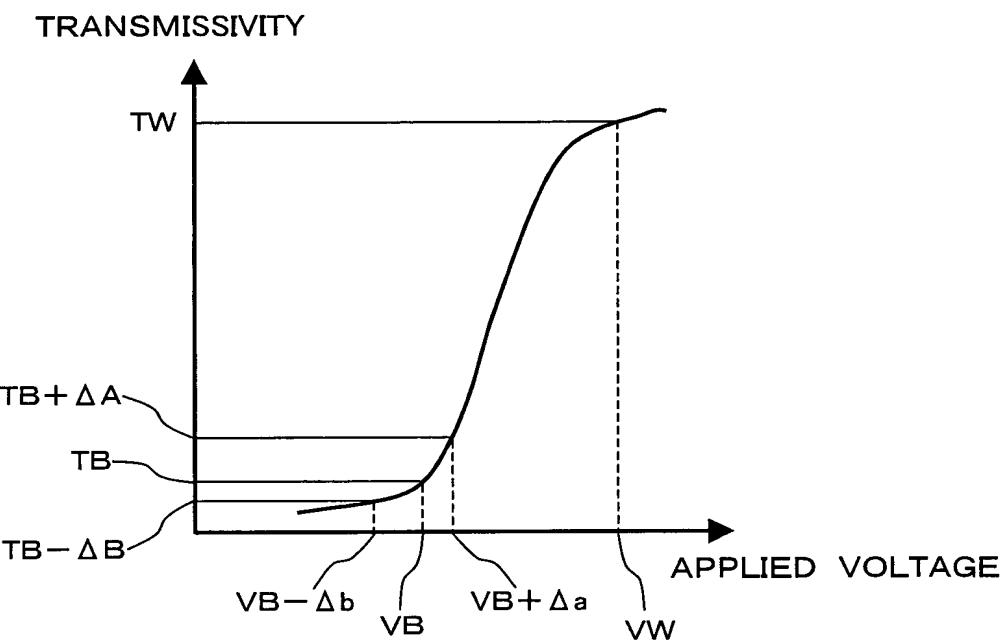
FIG. 10

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FIG.11



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FIG.12A

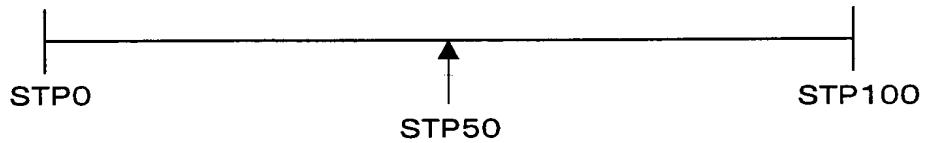


FIG.12B

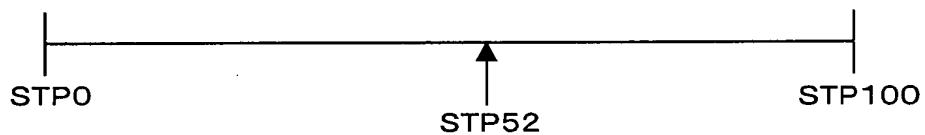


FIG.12C

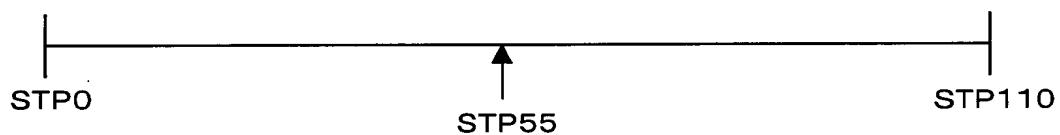


FIG.12D

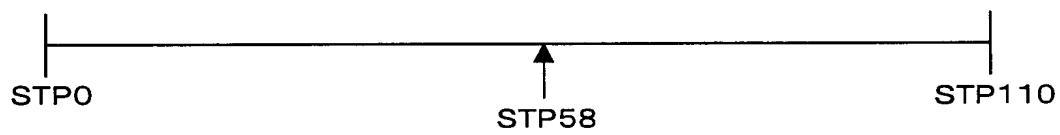
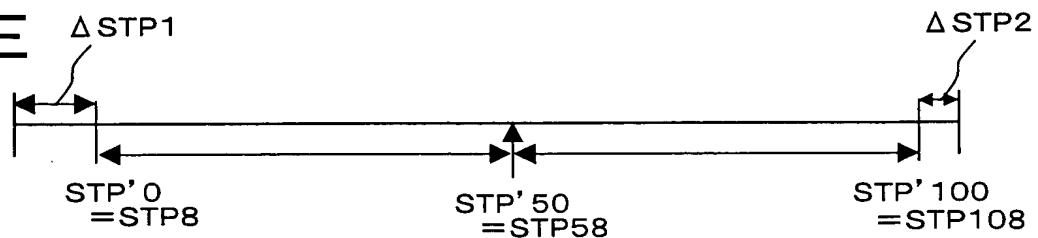


FIG.12E



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FIG.13A

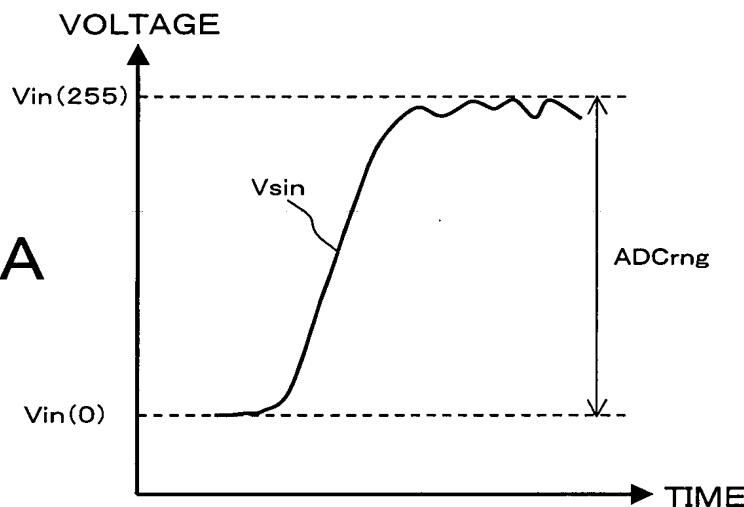


FIG.13B

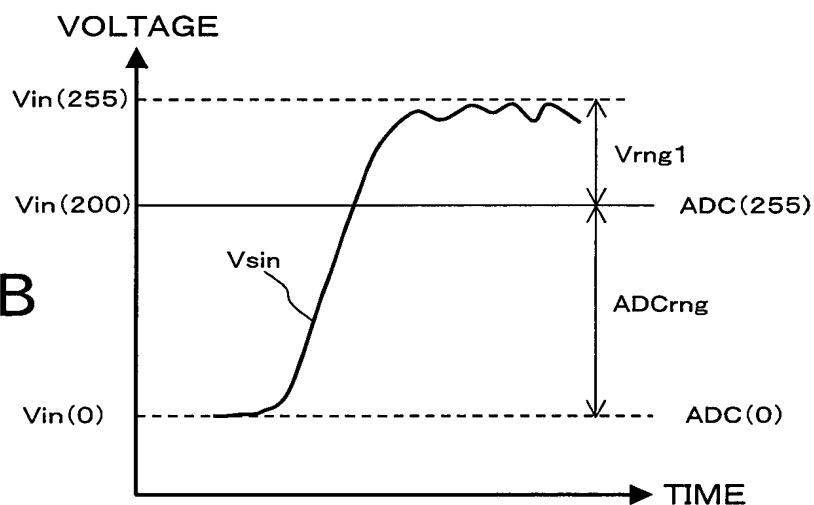
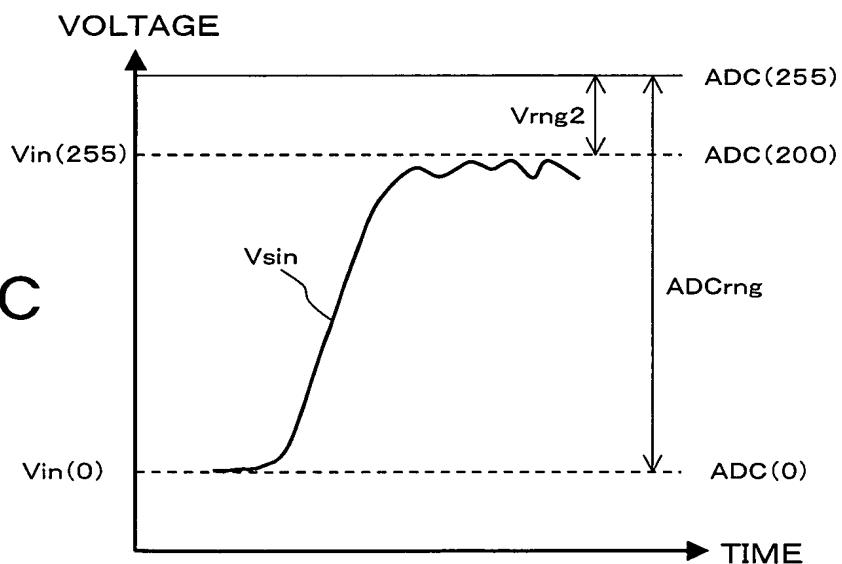
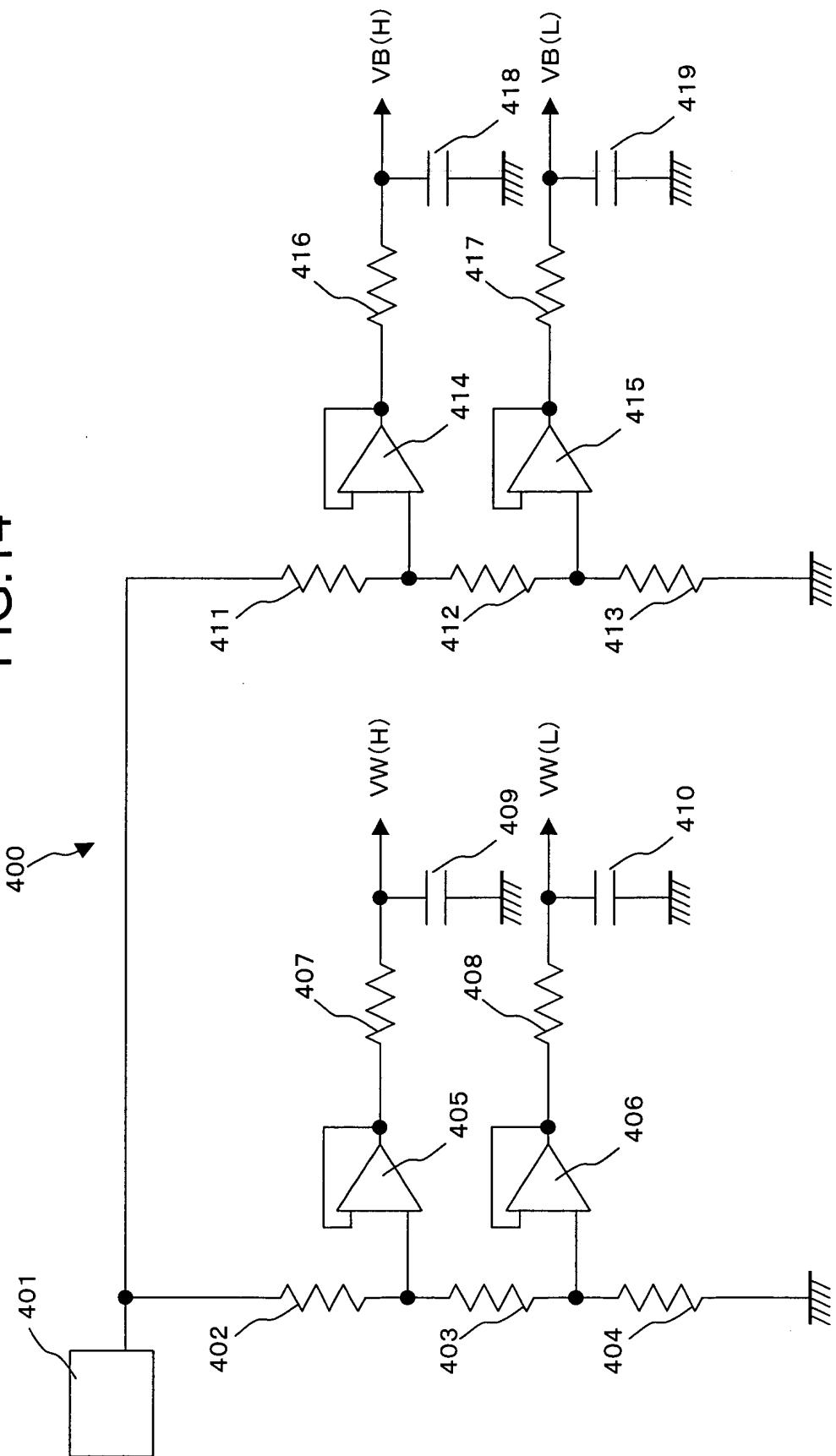


FIG.13C



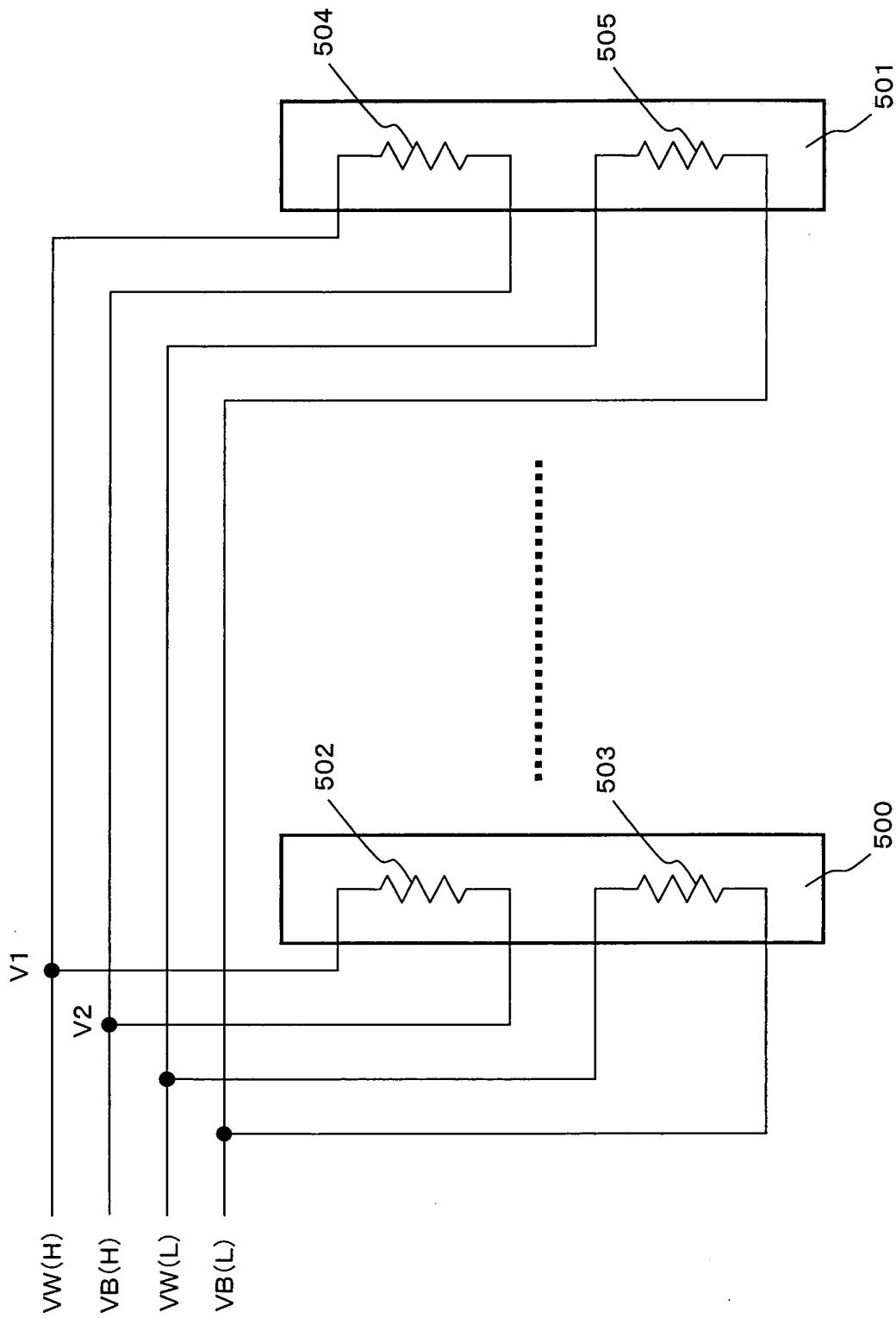
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FIG.14



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FIG. 15



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FIG.16

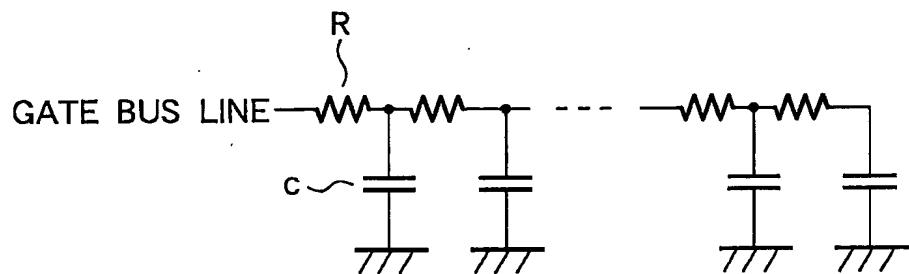


FIG.17

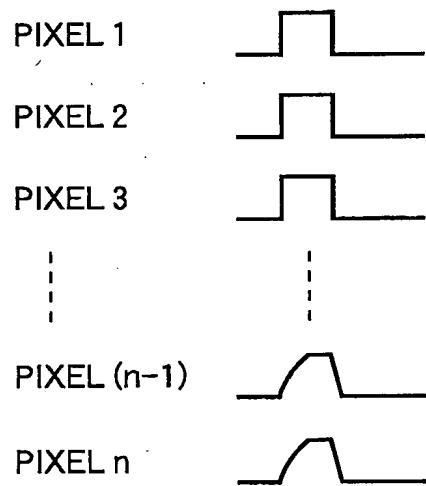


FIG.18A

HORIZONTAL
SYNCHRONIZING
SIGNAL a

FIG.18B

HORIZONTAL
SYNCHRONIZING
SIGNAL b

FIG.18C

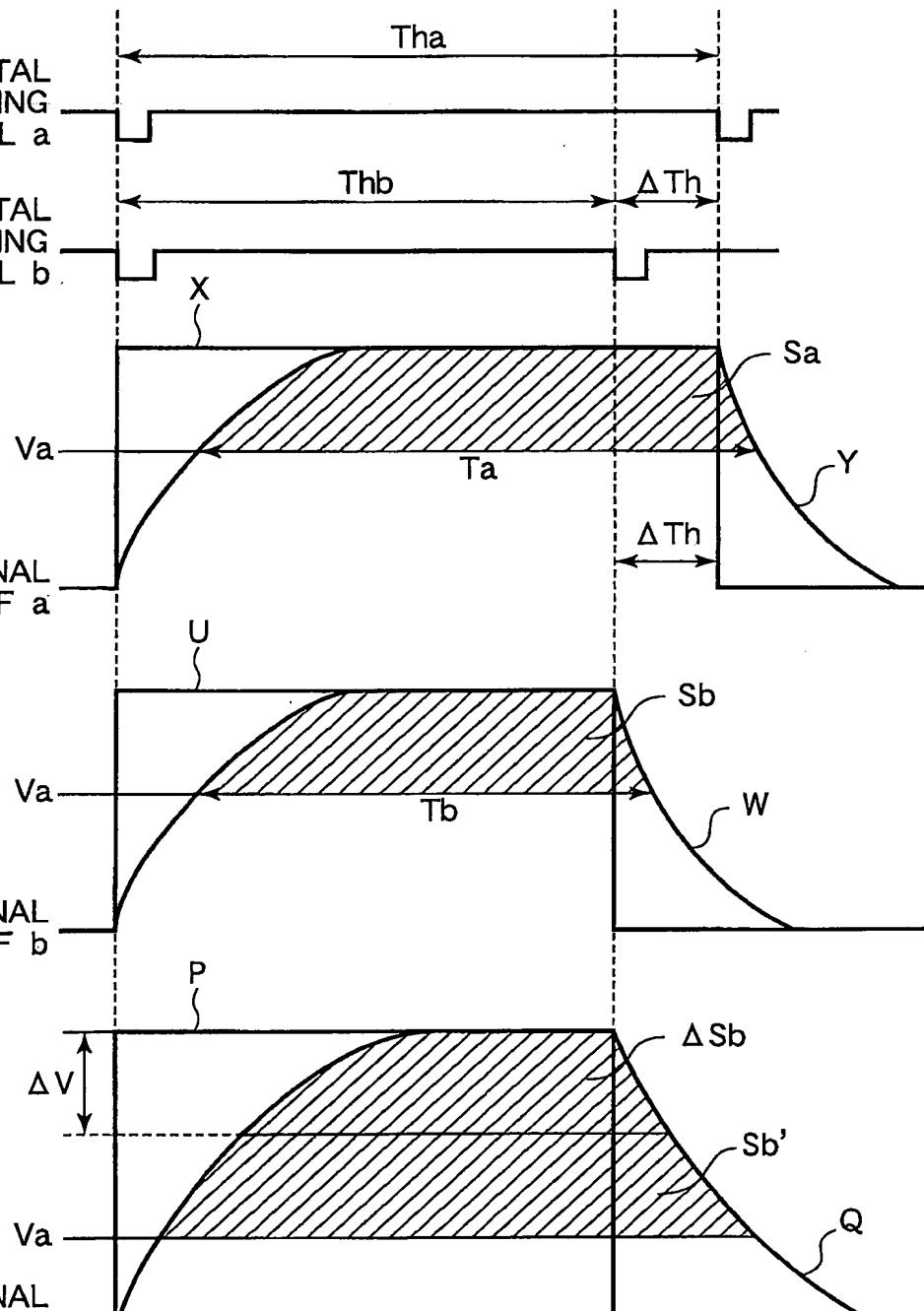
GATE SIGNAL
IN THE CASE OF a

FIG.18D

GATE SIGNAL
IN THE CASE OF b

FIG.18E

GATE SIGNAL
IN THE CASE
WHERE VOLTAGE
IS RAISED BY ΔV



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FIG.19

